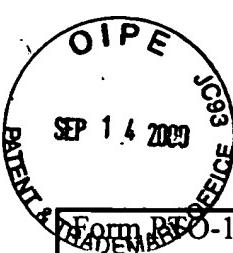




Form PTO-1449 O I P E INFORMATION-DISCLOSURE SEP 1 5 2000 STATEMENT		Docket No: 112025-0174	Serial No. 09/535,810			
		Applicant: Silvano Gai et al.				
		Filed: March 28, 2000		Group Not Yet Assigned		
U. S. Patent Documents						
Ex.	Doc. No.	Date	Name	Class	Subcl.	Filed
PW	4,071,887	01/31/78	Daly et al.	364	200	10/30/75
PW	4,543,630	09/24/85	Neches	364	200	04/19/84
PW	4,833,468	05/23/89	Larson et al.	340	825.800	10/14/87
PW	4,663,748	05/05/87	Karbowiak et al.	370	89	04/08/85
PW	4,754,395	06/28/88	Weisshaar et al.	364	200	10/06/88
PW	4,539,679	09/03/85	Bux et al.	370	88	11/23/83
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PW	4,704,680	11/03/87	Saxe	364	200	08/15/84
PW	4,747,130	05/24/88	Ho	379	269	12/17/88
PW	4,549,291	10/22/85	Renoulin et al.	370	89	09/09/83
PW	4,495,617	01/22/85	Ampulski et al.	370	86	09/09/82
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PW		Computer Architecture <i>The Anatomy of Modern Processors</i> , Pipeline Hazards, world wide web page http://ciips.ee.uwa.edu.au/~morris/CA406/pipe_hazard.html , 1999, pages 1-6.				
PW		<i>Cisco Catalyst Workgroup Switch Version 3.0</i> , web, http://www.cisco.com/warp/public/558/16.html , pgs. 1-5.				
PW		Cisco VLAN Roadmap, web, http://www.cisco.com/warp/public/538/7.html , pgs. 1-9.				
Examiner: <i>[Signature]</i>			Date Considered: <i>7/23/03</i>			
<p>*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>						



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INFORMATION-DISCLOSURE

STATEMENT

Docket No: 112025-0174

Serial No. 09/535,810

Applicant: Silvano Gai et al.

Filed: March 28, 2000

Group Not Yet Assigned

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PW	4,512,014	04/16/85	Binz et al.	370	84	08/06/80
PW	4,930,121	05/29/90	Shiobara	370	85.4	09/09/88
PW	4,713,807	12/15/87	Caves et al.	370	94	05/22/86
PW	4,709,364	11/24/87	Hasegawa et al.	370	85	07/25/85
PW	4,680,757	07/14/87	Murakami et al.	370	89	04/15/86
PW	4,609,920	09/02/86	Segarra	340	825.51	05/13/83
PW	4,604,742	08/05/86	Hamada et al.	370	89	04/21/84
PW	4,404,557	09/13/83	Grow	340	825.05	03/05/82
PW	4,530,091	07/16/85	Crockett	370	60	07/08/83
PW	4,677,614	06/30/87	Circo	370	86	02/15/83

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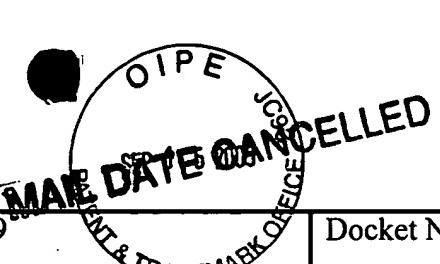
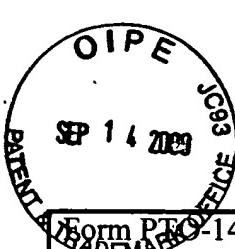
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PW	IAC (SM) Newsletter Database (TM) Communications Industry Researchers, Broadband Networks and Applications, copyright 1995 Information Access Company, a Thomson Corporation Company, Section No. 16, Vol. E; ISSN: 1059-0544, Headline, Cisco Announces New Fast Ethernet Interface, pgs. 2-3.
PW	IAC (SM) Newsletter Database (TM) Communications Industry Researchers, Broadband Networks and Applications, copyright 1995 Information Access Company, a Thomson Corporation Company, Section No. 16, Vol. 3; ISSN: 1059-0544, Headline, Cisco Announces Token-Ring Switching Products, pgs 4-5.
PW	Cisco 7000 Series Gains Fast Ethernet Interface, Becomes Only Router Family to Support Three High-Speed Network Types, web, http://www.cisco.com/warp/public/146/199.html pgs. 1-2.

Examiner:

Date Considered:

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02/09

INFORMATION-DISCLOSURE

STATEMENT

Docket No: 112025-0174

Serial No. 09/535,810

Applicant: Silvano Gai et al.

Filed: March 28, 2000

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pn	4,771,423	09/13/88	Ohya et al.	370	86	03/13/86
pn	4,536,876	08/20/85	Bahr et al.	370	86	02/10/84
pn	4,746,920	05/24/88	Nellen et al.	340	825.140	03/28/86
pn	4,590,554	05/20/86	Glazer et al.	364	200	11/23/82
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pn			ISL Functional Specification, web, http://www.cisco.com/warp/public/741/4.htm , pgs. 1-4
pn			Cisco-Catalyst 5000 Software Release 2.1 Feature Set, web, http://www.cisco.com/warp/public/729/c5000/426_pp.htm , pgs. 1-9
pn			IEEE Standard Project P802.1Q, Draft Standard for Virtual Bridged Local Area Networks, Copyright by the Institute of Electrical and Electronics Engineers, Inc., February 28, 1997, pgs. 1-88.

Examiner:

Date Considered:

7/23/03

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INFORMATION-DISCLOSURE

STATEMENT

Docket No: 112025-0174

Serial No. 09/535,810

Applicant: Silvano Gai et al.

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Group Not Yet Assigned

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p)	4,870,568	09/26/89	Kahle et al.	364	200	06/25/86
p)	4,922,486	05/01/90	Lidinsky et al.	370	60	03/31/88
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p)	4,993,028	02/12/91	Hillis	371	39.1	09/07/88
p)	5,070,446	12/03/91	Salem	395	500	02/08/91
p)	5,093,801	03/03/92	White et al.	364	726	07/06/90
p)	5,111,198	05/05/92	Kuszmaul	340	825.52	12/18/91
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OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc)

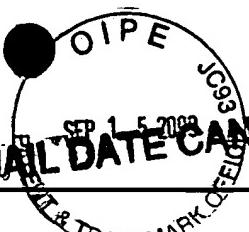
p)		Draft Standard P. 802.1Q/D10, IEEE Standards for Local and Metropolitan Area Networks: Virtual Bridged Local Area Networks, Copyright by the Institute of Electrical and Electronics Engineers, Inc., March 22, 1997, pgs.1-212.
p)		ISL Configurations for Cisco IOS and the Catalyst 5000, web, http://www.cisco.com/warp/public/741/8.html , pgs. 1-8.
p)		Dynamic Inter-Switch Link Protocol, 2.2 Configuration Note, pgs. 2-6.

Examiner:

Date Considered:

3/21/02

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STATEMENT

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Serial No. 09/535,810

Applicant: Silvano Gai et al.

Filed: March 28, 2000

Group Not Yet Assigned

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Ex.	Doc. No.	Date	Name	Class	Subcl.	Filed
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pj	5,148,547	09/15/92	Kahle et al.	395	800	05/17/91
pj	5,151,996	09/29/92	Hillis	395	800	03/20/90
pj	5,157,663	10/20/92	Major et al.	371	9.1	09/24/90
pj	5,175,865	12/29/92	Hillis	395	800	07/01/93
pj	5,212,773	05/18/93	Hillis	395	200	02/22/93
pj	5,222,216	06/22/93	Parish et al.	395	275	07/12/93
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pj	5,247,613	09/21/93	Bromley	395	200	05/28/93
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pj	5,255,291	10/19/93	Holden et al.	375	111	01/14/88

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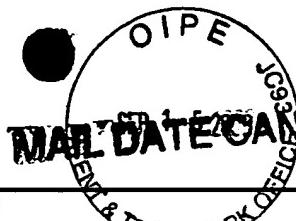
pj			<i>Virtual LANs, Catalyst 2820 Series and Catalyst 1900 Series Enterprise Edition Software Configuration Guide, Chapter 2, pgs.2-1 -- 2-49.</i>
pj			<i>Configuring VTP and Virtual LANs, Catalyst 5000 Series Software Configuration Guide, Chapter 13, pgs.13-1 – 13-24.</i>
pj			<i>IEEE 1149.1 Device Architecture, http://www.asset-intertech.com/homepage/bscantur/arch.htm, pp. 1-3, 1998</i>

Examiner:

Date Considered:

3/23/03

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Form RPO-1449

Docket No: 112025-0174

Serial No. 09/535,810

INFORMATION-DISCLOSURE
STATEMENT

Applicant: Silvano Gai et al.

Filed: March 28, 2000

Group Not Yet Assigned

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p)	5,265,207	11/23/93	Zak et al.	395	200	04/08/93
p)	5,274,631	12/28/93	Bhardwaj	370	60	03/11/91
p)	5,289,156	02/22/94	Ganmukhi	340	146.2	06/16/93
p)	5,295,258	03/15/94	Jewett et al.	395	575	01/05/90
p)	5,301,310	04/05/94	Isman et al.	395	575	02/07/91
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p)	5,355,492	10/11/94	Frankel et al.	395	700	11/05/91
p)	5,357,612	10/18/94	Alaiwan	395	200	01/11/93
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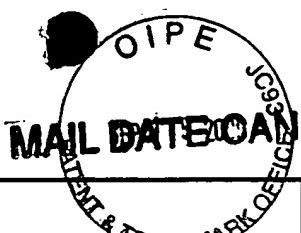
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p)	IEEE 1149.1 Device Architecture (continued), Using the Instruction Register (IR), http://www.asset-intertech.com/homepage/bscantur/arch2.htm#UsingIR , pp. 1-4, 1998
p)	The Principle of Boundary-Scan Architecture, http://www.asset-intertech.com/homepage/bscantur/principl.htm , pp. 1-4, 1998
p)	IEEE 1149.1 Device Architecture (continued), The Test Access Port (TAP), http://www.asset-intertech.com/homepage/bscantur/arch3.htm#TAP , pp. 1-3, 1998

Examiner:

Date Considered: 7/23/03

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PTD-1449	Docket No: 112025-0174	Serial No. 09/535,810
INFORMATION-DISCLOSURE	Applicant: Silvano Gai et al.	
STATEMENT	Filed: March 28, 2000	Group Not Yet Assigned

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Ex.	Doc. No.	Date	Name	Class	Subcl.	Filed
PW	5,388,214	02/07/95	Leiserson et al.	395	200	01/14/94
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PW	5,404,296	04/04/95	Moorhead	364	421	09/25/91
PW	5,404,562	04/04/95	Heller et al.	395	800	10/01/93
PW	5,410,723	04/25/95	Schmidt et al.	395	800	05/21/93
PW	5,455,932	10/03/95	Major et al.	395	489	10/20/92
PW	5,485,627	01/16/96	Hillis	395	800	12/28/92
PW	5,530,809	06/25/96	Douglas et al.	395	200.2	01/14/94
PW	5,535,408	07/09/96	Hillis	395	800	05/02/94
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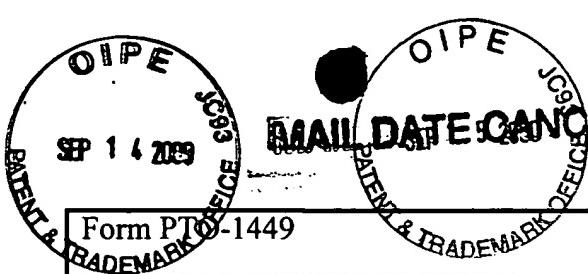
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PW	Digital Systems Testing and Testable Design, Miron Abramovici, et al., Computer Science Press, pp. 342-409, Copyright 1990
PW	World Wide Web page http://www.synopsys.com/products/analysis/primtime_ds.html , Prime Time, July 12, 1999, pages 1-7.
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Examiner: *[Signature]* Date Considered: *8/28/03*

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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INFORMATION-DISCLOSURE

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Docket No: 112025-0174

Serial No. 09/535,810

Applicant: Silvano Gai et al.

Filed: March 28, 2000

Group Not Yet Assigned

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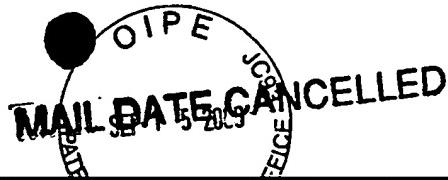
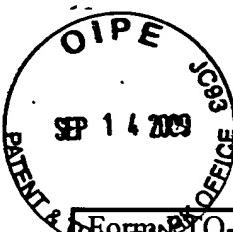
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p			World Wide Web page http://www.synopsys.com/products/analysis/sta_wp.html , <i>Static Timing Verification: Methodology Overview</i> , July 12, 1999, pages 1-13.
p			World Wide Web page http://www.synopsys.com/products/analysis/pt_tech_bgr.html , <i>Prime Time Full-Chip Static Timing Analyzer</i> , July 12, 1999, pages 1-17.
p			Pankaj Gupta and Nick McKeown, Packet Classification on Multiple Fields
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<p>*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>			

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Ex.		Doc. No.	Date	Name	Class	Subcl.	Filed
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pn		5,909,441	06/01/99	Alexander, Jr. et al.	370	395	04/11/97
pn		5,909,550	06/01/99	Shankar et al.	395	200.57	10/16/96
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<p>*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>							



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INFORMATION-DISCLOSURE
STATEMENT

Docket No: 112025-0174

Serial No. 09/535,810

Applicant: Silvano Gai et al.

Filed: March 28, 2000

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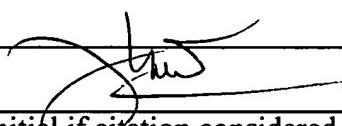
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Ex.	Doc. No.	Date	Name	Class	Subcl.	Filed
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pn	4,864,559	9/05/89	Perlman	370	60	09/27/88
pn	5,027,350	6/25/91	Marshall	370	85.13	10/18/89
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pn	5,511,168	4/23/96	Perlman et al	395	200.15	07/1/93
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Foreign Patent Documents

	Date	Country	Class	Subcl.	Filed

OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc)

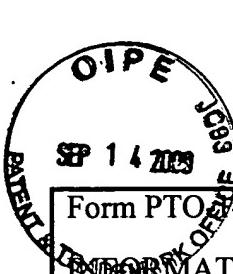
Examiner: 

Date Considered:

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	Applicant: Silvano Gai et al.	
	Filed: March 28, 2000	Group Not Yet Assigned

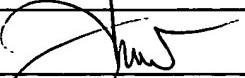
U. S. Patent Documents

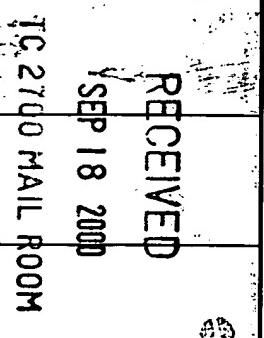
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PN	5,633,858	05/27/97	Chang et al	370	255	02/29/96
PN	5,684,800	11/04/97	Dobbins et al	370	401	11/15/95
PW	5,740,171	04/14/98	Mazzola et al	370	392	03/28/96
PW	5,742,604	04/21/98	Edsall et al	370	401	03/28/96
PW	5,764,636	06/09/98	Edsall	370	401	03/28/96
PW	5,018,137	05/21/91	Backes et al	370	85.13	10/16/90
PT	5,359,592	10/25/94	Corbalis et al.			
PW	5,280,470	1/18/94	Buhrke et al.			
PW	5,224,099	6/29/93	Corbalis et al.			
PW	5,014,265	5/7/91	Hahne et al.			
PW	5,796,732	8/18/98	Mazzola et al.			

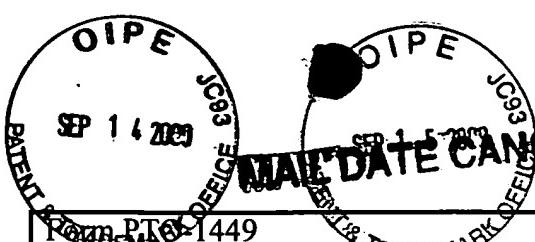
Foreign Patent Documents

		Date	Country	Class	Subcl.	Filed

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Examiner:		Date Considered:	2/23/03
<p>*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>			





MAIL DATE CANCELLED

PTAB 1449 INFORMATION-DISCLOSURE STATEMENT	Docket No: 112025-0174	Serial No. 09/535,810
	Applicant: Silvano Gai et al.	
	Filed: March 28, 2000	Group Not Yet Assigned

U. S. Patent Documents

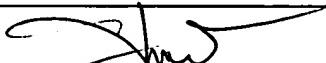
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pn	5,568,380	10/22/96	Brodnax et al.	364	184	08/30/93
pn	5,325,487	06/28/94	Au et al.	395	250	08/14/90
pn	4,954,988	09/04/90	Robb	365	189.02	10/28/88
pn	5,872,963	02/16/99	Bitar et al.	395	580	02/18/97
pn	5,748,936	05/05/98	Karp et al.	395	394	05/30/96
pn	5,787,243	07/28/98	Stiffler	395	182.11	07/02/96
pn	5,812,811	09/22/98	Dubey et al.	395	392	02/03/95
pn	5,349,680	09/20/94	Fukuoka	395	800	10/31/91
ps	4,922,418	05/01/90	Dolecek	364	200	01/15/88
pn	4,954,988	09/04/90	Robb	365	189.02	10/28/88
pn	5,325,487	06/28/94	Au et al.	395	250	08/14/90

Foreign Patent Documents

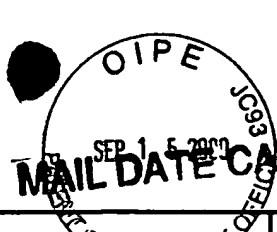
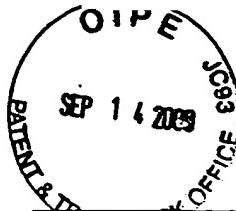
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Form PTO-1449

INFORMATION-DISCLOSURE

STATEMENT

Docket No: 112025-0174

Serial No. 09/535,810

Applicant: Silvano Gai et al.

Filed: March 28, 2000

Group Not Yet Assigned

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pn	5,349,680	09/20/94	Fukuoka	395	800	10/31/91
pn	5,475,856	12/12/95	Kogge	395	800	10/17/94
pn	5,568,380	10/22/96	Brodnax et al.	364	184	08/30/93
pn	5,613,136	03/18/97	Casavant et al.	395	800	10/29/93
pn	5,748,963	05/05/98	Karp et al.	395	394	05/30/96
pn	5,781,753	07/14/98	McFarland et al.	395	394	03/13/95
pn	5,787,243	07/28/98	Stiffler	395	182.11	07/02/96
pn	5,812,811	09/22/98	Dubey et al.	395	392	02/03/95
pn	5,832,291	11/03/98	Rosen et al.	395	800.11	12/15/95
pn	5,860,086	01/12/99	Crump et al.	711	109	06/04/97
pn	5,872,963	02/16/99	Bitar et al.	395	580	02/18/97

Foreign Patent Documents

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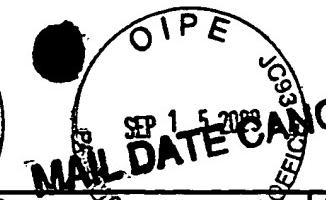
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PTO TRADEMARKS SEARCHED PATENT & TRADEMARK OFFICE USA SEP 14 2000	Docket No: 112025-0174	Serial No. 09/535,810
INFORMATION-DISCLOSURE STATEMENT	Applicant: Silvano Gai et al.	
	Filed: March 28, 2000	Group Not Yet Assigned

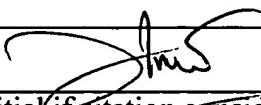
U. S. Patent Documents

Ex.	Doc. No.	Date	Name	Class	Subcl.	Filed
1	5,960,211	09/28/99	Schwartz et al.	395	800.22	12/15/95
2	6,035,422	03/07/2000	Hohl et al.	714	35	05/15/97
3	5,953,512	09/14/99	Cai et al.	395	381	12/29/97
4	5,499,384	03/12/96	Lentz et al.	395	821	12/31/92
5	5,440,752	08/08/95	Lentz et al.	395	800	07/08/91
6	5,428,761	06/27/95	Herlihy et al.	395	425	03/12/92
7	5,067,078	11/19/91	Talgam et al.	395	400	04/17/89
8	5,566,170	10/15/96	Bakke et al.	370	60	12/29/94
9	5,941,988	08/24/99	Bhagwat et al.	713	201	06/27/97
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11	6,067,569	05/23/00	Khaki et al.	709	224	07/10/97

Foreign Patent Documents

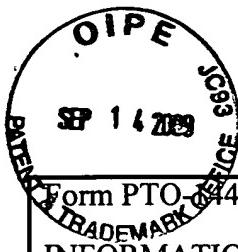
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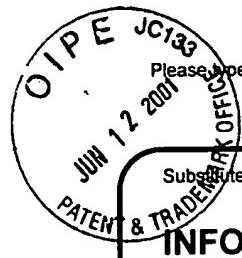
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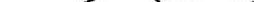
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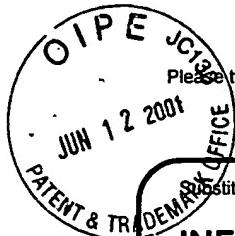
Substitute for form 1449A/PTO <i>& TRADEMARK INFORMATION DISCLOSURE STATEMENT BY APPLICANT</i> <i>(use as many sheets as necessary)</i>		Complete if Known													
Sheet	1	of	3												
<table border="1"> <tr> <td>Application Number</td> <td>09/535,810</td> </tr> <tr> <td>Filing Date</td> <td>March 28, 2000</td> </tr> <tr> <td>First Named Inventor</td> <td>Silvano Gai et al.</td> </tr> <tr> <td>Group Art Unit</td> <td>Not Yet Assigned</td> </tr> <tr> <td>Examiner Name</td> <td>Not Yet Assigned</td> </tr> <tr> <td>Attorney Docket Number</td> <td>112025-0174</td> </tr> </table>				Application Number	09/535,810	Filing Date	March 28, 2000	First Named Inventor	Silvano Gai et al.	Group Art Unit	Not Yet Assigned	Examiner Name	Not Yet Assigned	Attorney Docket Number	112025-0174
Application Number	09/535,810														
Filing Date	March 28, 2000														
First Named Inventor	Silvano Gai et al.														
Group Art Unit	Not Yet Assigned														
Examiner Name	Not Yet Assigned														
Attorney Docket Number	112025-0174														

Examiner Signature		Date Considered	7/23/03
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Sheet 2 of 3

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Application Number	09/535,810
Filing Date	March 28, 2000
First Named Inventor	Silvano Gai et al.
Group Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	112025-0174

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

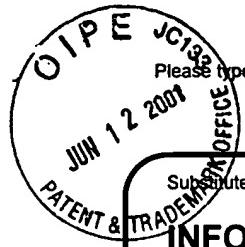
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
pn		Application Prioritization, Top Layer Networks, Inc., (c) 1999, pp.1-8	
pn		Application Control, Top Layer Networks, Inc., (c) 1999, pp.1-8	
pn		Classifying Custom Applications with 7 Layer Application Control, Top Layer Networks, Inc., (c) 1999, pp.1-8	
pn		Custom ASIC Design Enables 7 Layer Application Control, Top Layer Networks, Inc., (c) 1999, pp.1-5	
pn		Implementing Qos for Business Driven Networks, Top Layer Networks, Inc., (c) 1999, pp.1-7	
pn		AppSwitch Data Sheet, Top Layer Networks, Inc., (c) 1999, pp.1-8	
pn		The Need for Classifying Applications Using 7 Layer Application Control, Top Layer Networks, Inc., (c) 1999, pp.1-19	
pn		Press Release-Data Communications and Internetwork Editors Name Top Layer Networks' "App Switch" A Winner of the Best of Show Awards at Networld + Interop 99 Las Vegas, Top layer Networks, Inc., (c) 1999, pp. 1-2	
pn		WALLACE, BOB, Load Balancing, http://www.computerworld.com/home/features.nf , ComputerWorld, (c) 1999, pp. 1-4	
pn		Foundry Networks Announces Net Generation Layer 4-7 Products, http://biz.yahoo.com/991115/ca_foundry_1.html , Business Wire, (c) 1994-1999, pp. 1-3	
pn		CURTONE, VIC, AppSwitch Does Layer 7 Switching, Network Computing, May 3, 1999, pp. 1-3	

Examiner Signature		Date Considered	7/23/01
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Sheet 3 of 3

<i>Complete if Known</i>	
Application Number	09/535,810
Filing Date	March 28, 2000
First Named Inventor	Silvano Gai et al.
Group Art Unit	2739
Examiner Name	Not Yet Assigned
Attorney Docket Number	112025-0174

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
pn		Layer 4 Switching: Unraveling the 'Vendorspeak', http://www.networking.com/shared/pr...cle? , Cisco Engineers, May 3, 1999, pp. 1-3	
pn		Regex, http://www.cs.utah.edu/dept/old/textinfo/regez , (c) 1992, pp. 1035	
pn		Perl5 Regular Expression Description, http://language.perl.com/all_about/regexprs.html , (c) 1994-1996, pp. 1-14	
pn		Perl Regular Expression Tutorial, http://www.meurrens.org/ip-Links/Java/regex/Perl/regex2a.html ., (c) 1994-1996, pp. 1-8	

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